Figure 1a

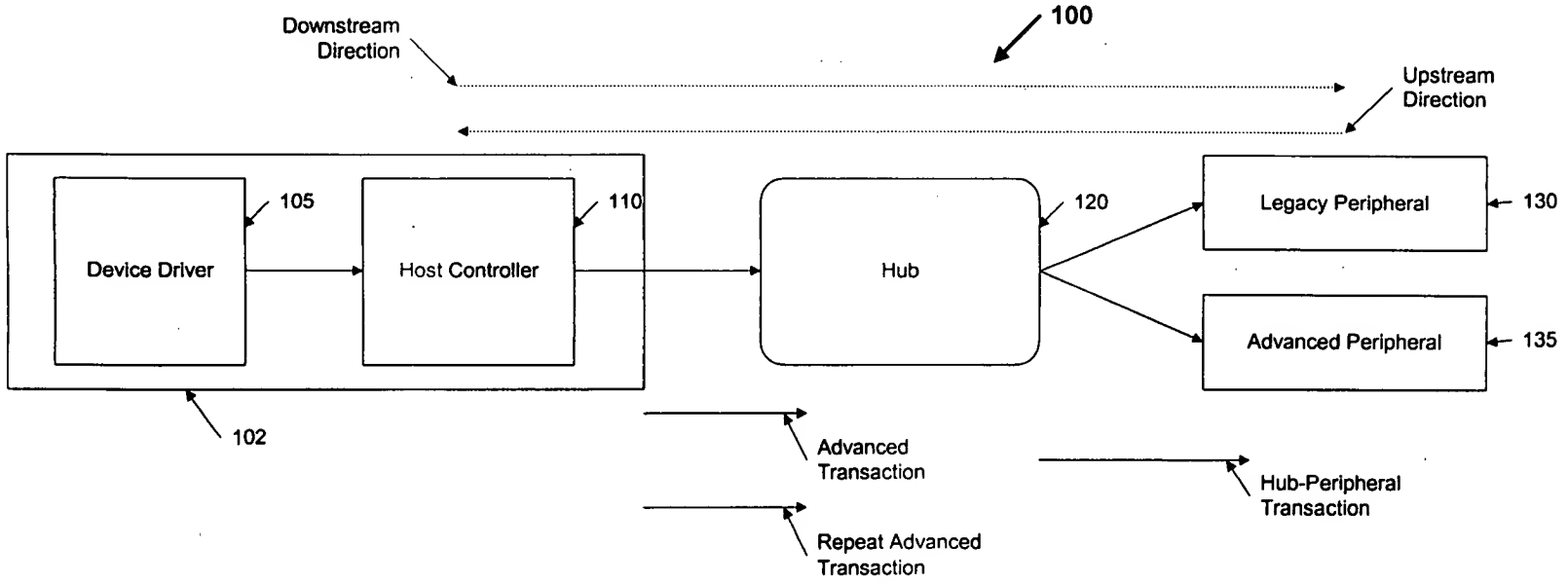

Figure 1b

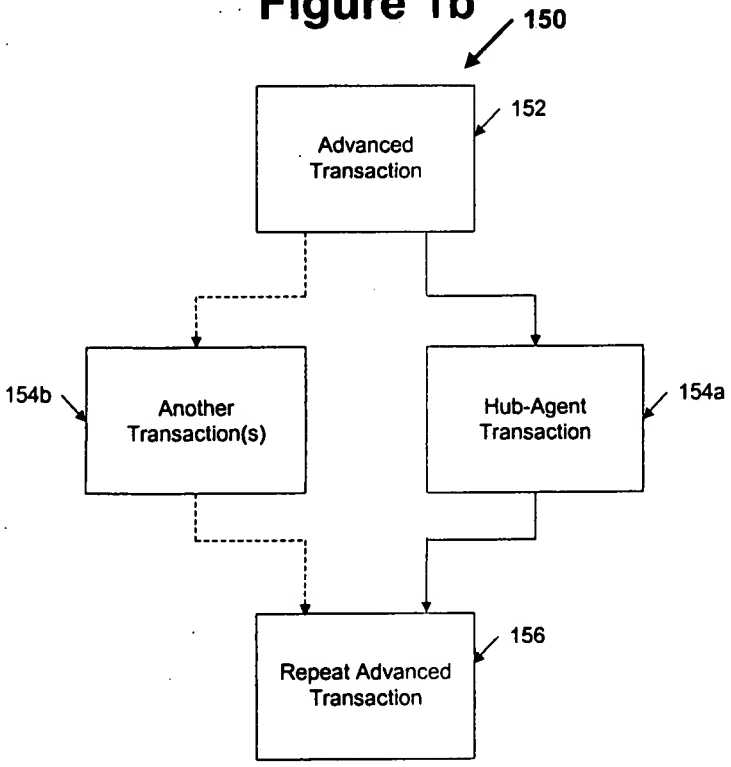

Figure 1c

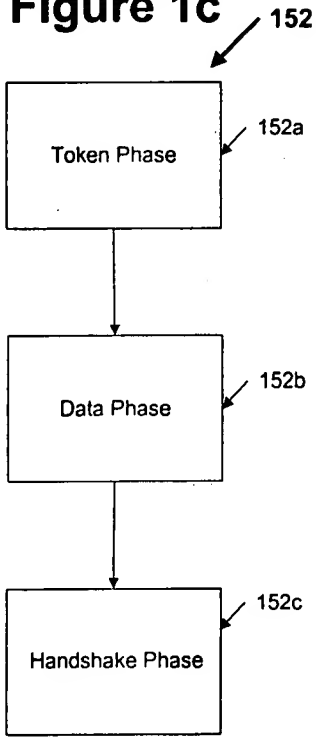

Figure 1d

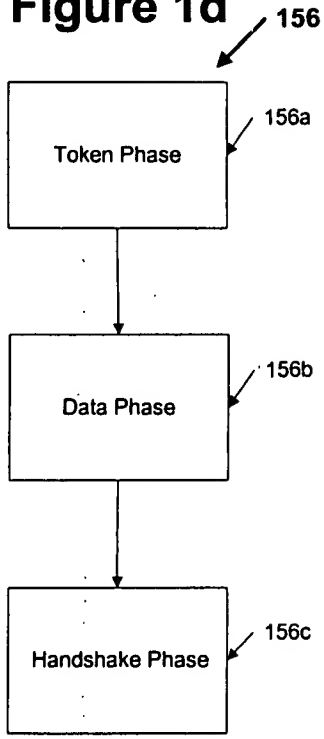

Figure 2a

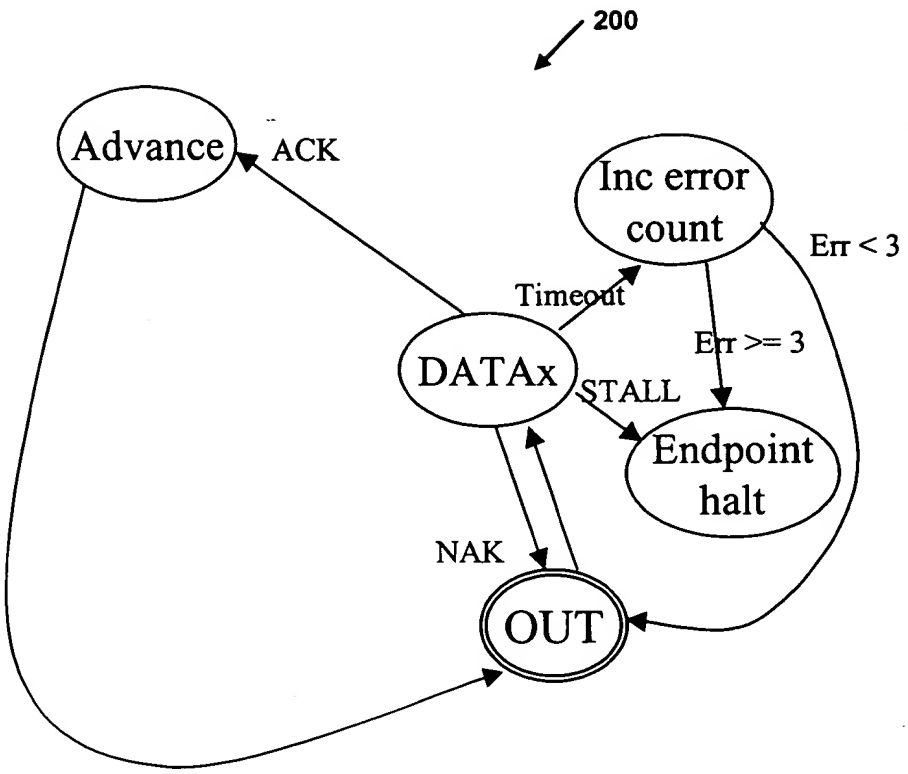

Figure 2b

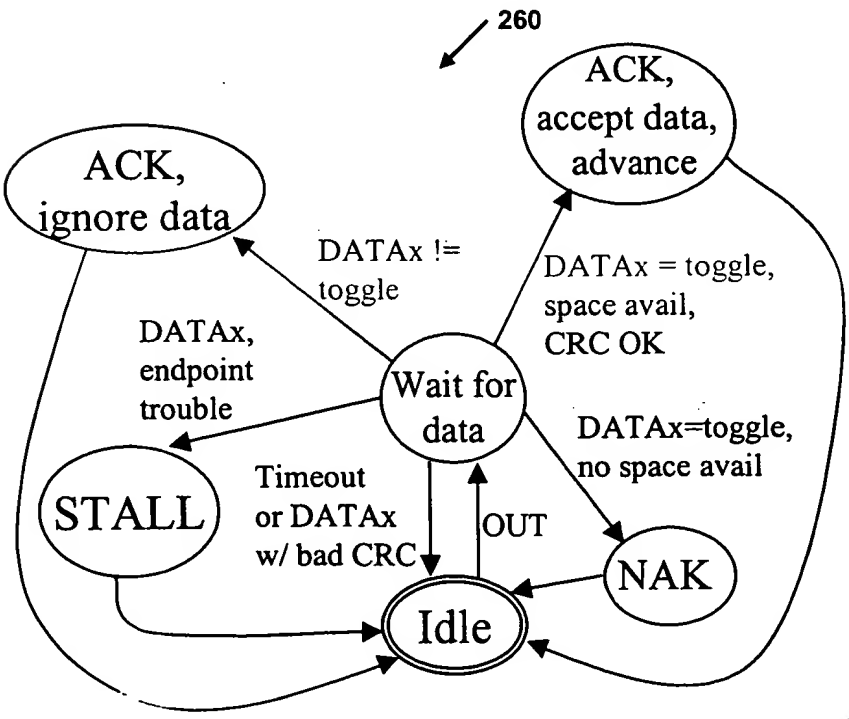

Figure 3a

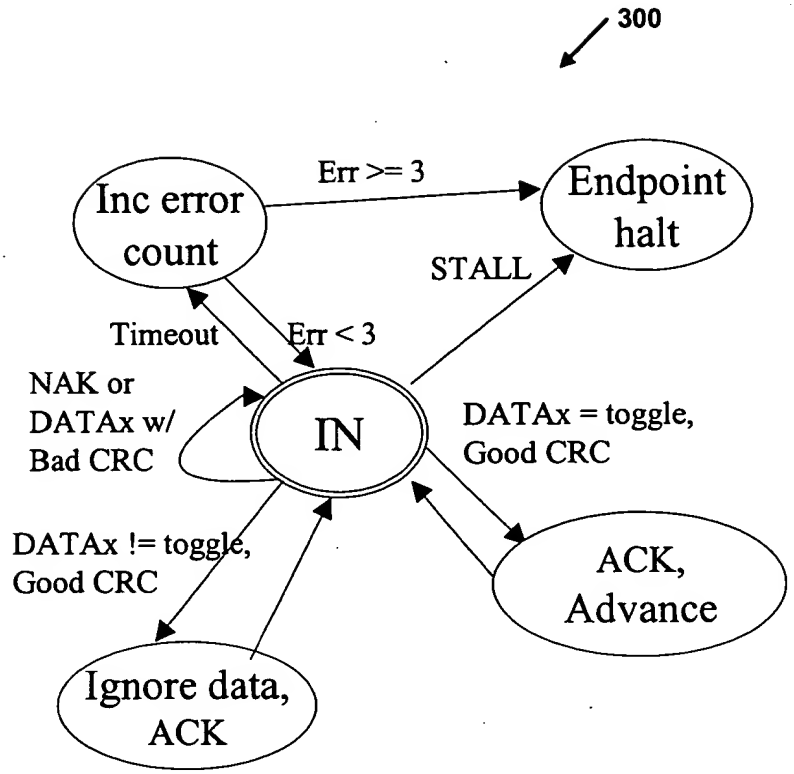

Figure 3b

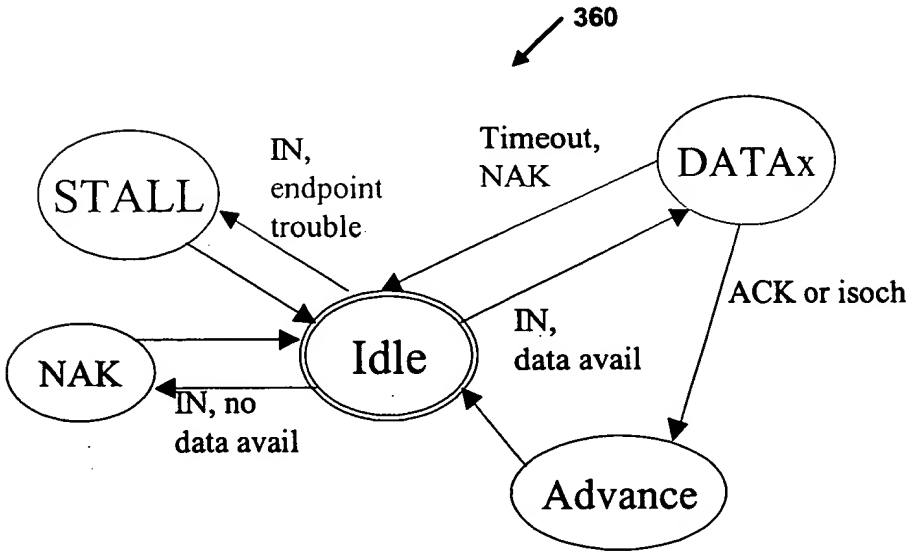

Figure 4a

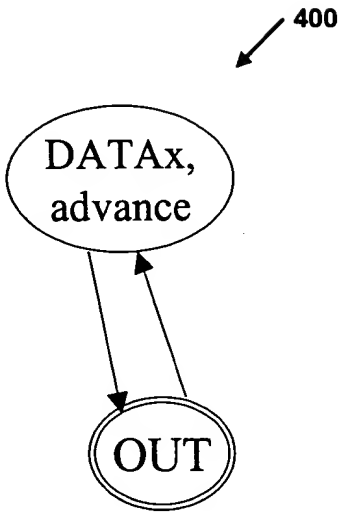

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Figure 4a

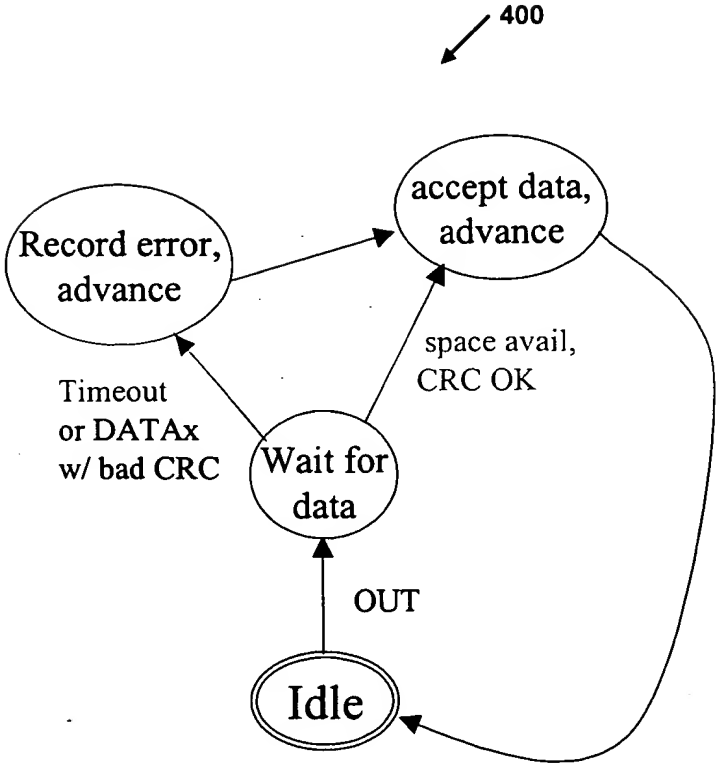

Figure 5a

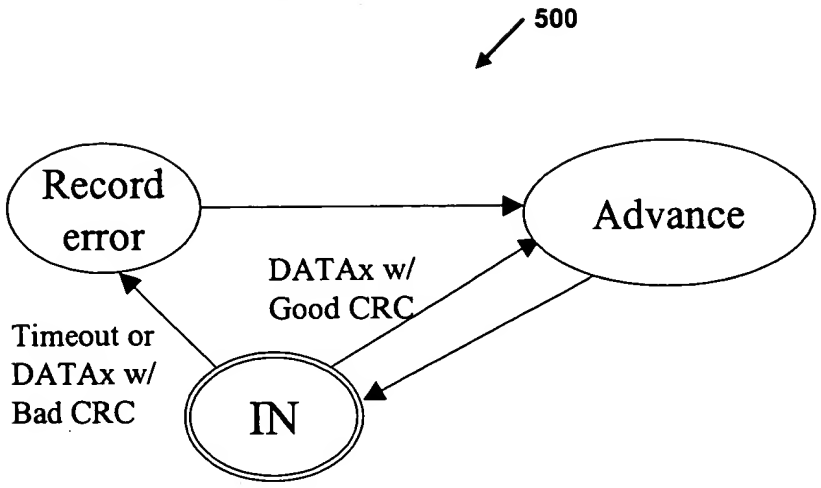

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Figure 5a

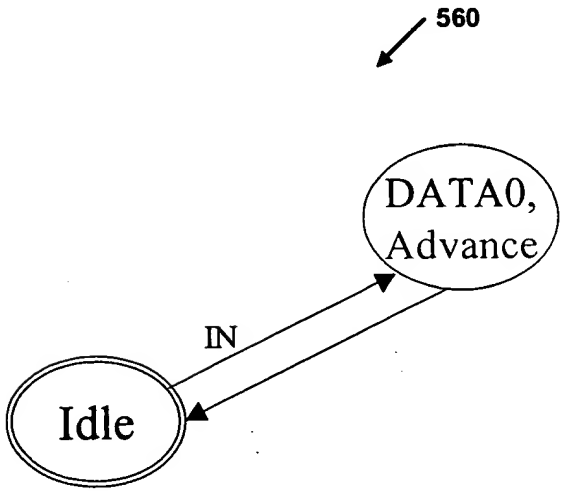
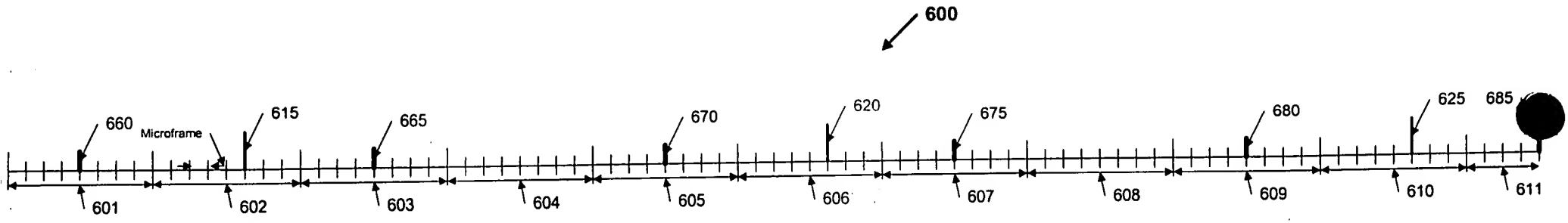
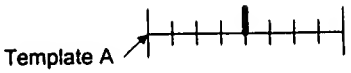
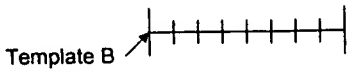


Figure 6



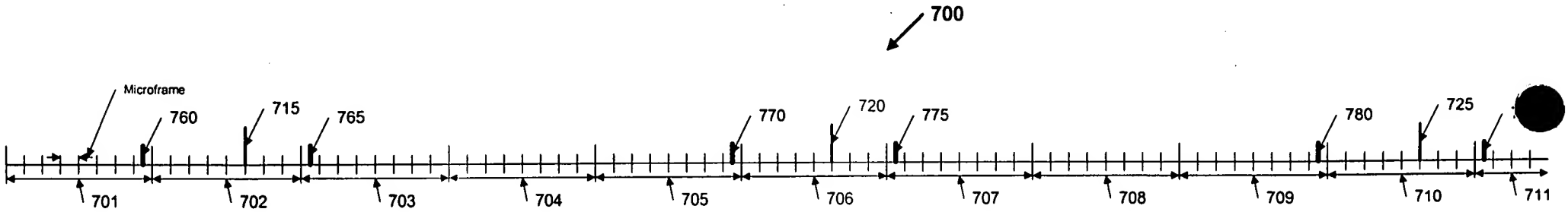


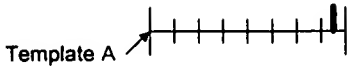
601, 603, 605, 607, 609, 611



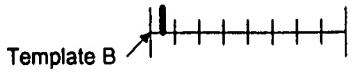
602, 604, 606, 608, 610

Figure 7

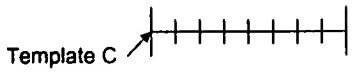




701, 705, 709



703, 707, 711



702, 706, 710

Figure 8

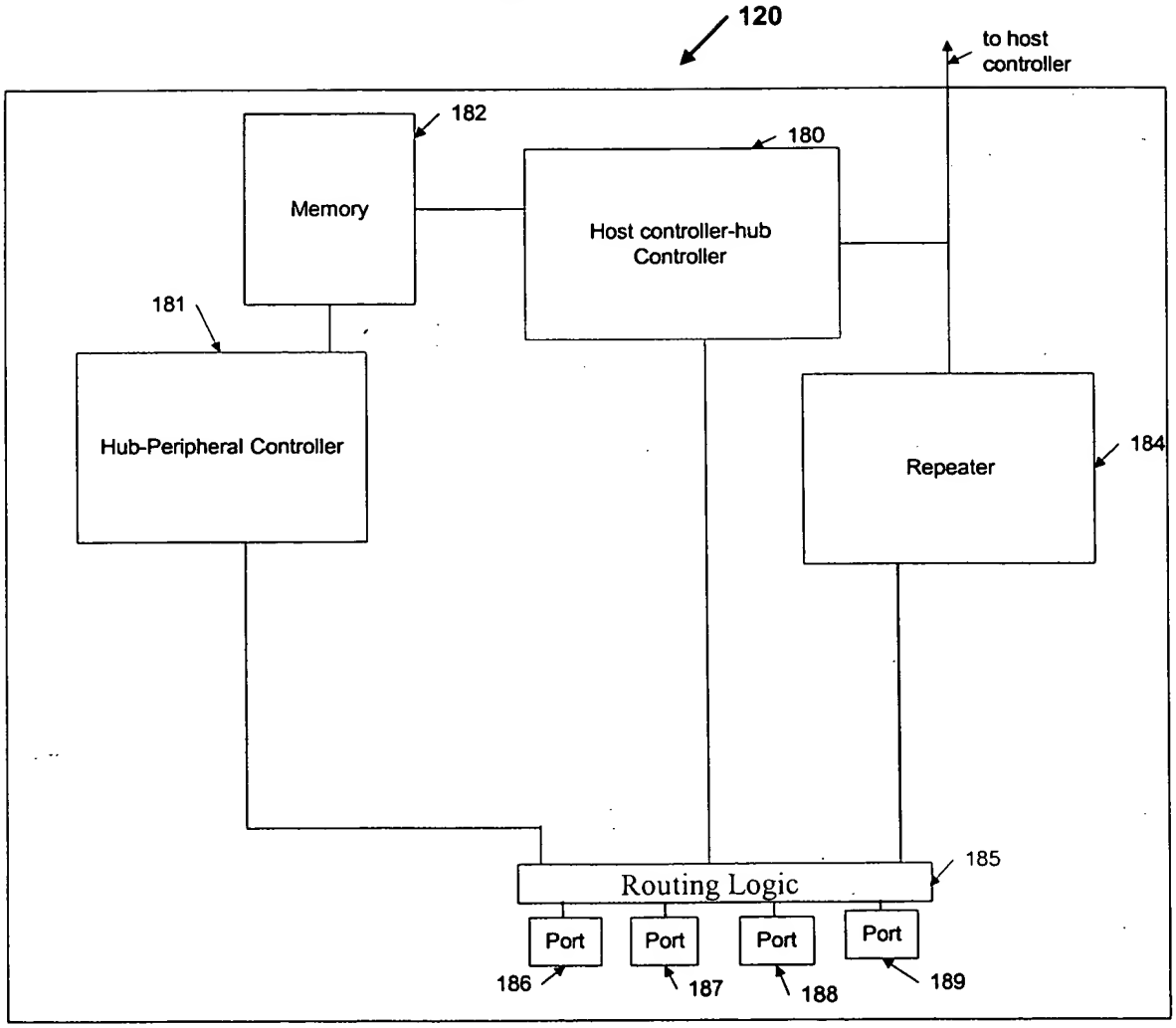

120
to host controller
182
Memory
180
Host controller-hub Controller
181
Hub-Peripheral Controller
Repeater
184
185
Routing Logic
Port
Port
Port
Port
186
187
188
189

Figure 9

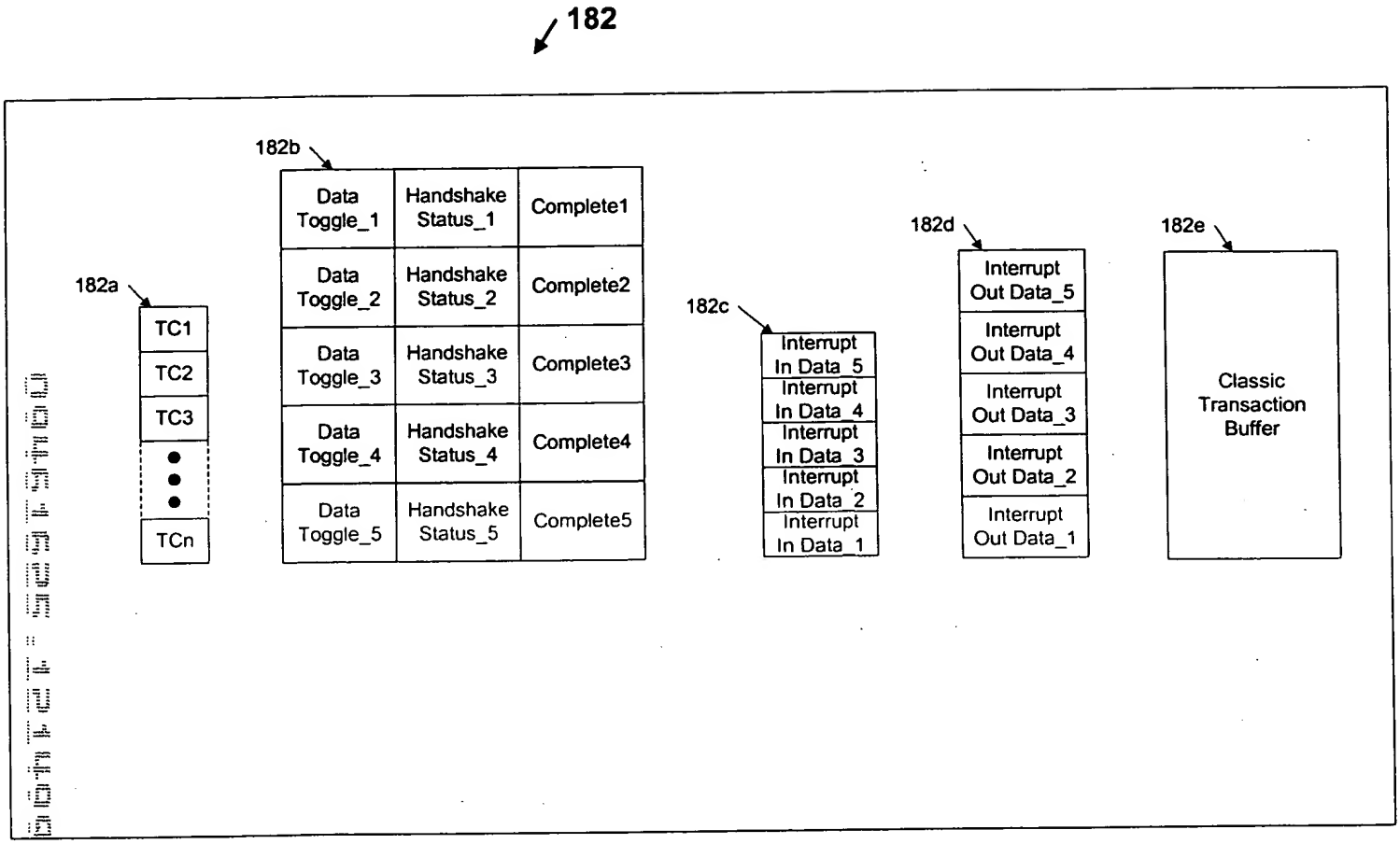

182
182a
TC1
TC2
TC3
TCn
182b
Data Toggle_1
Handshake Status_1
Complete1
Data Toggle_2
Handshake Status_2
Complete2
Data Toggle_3
Handshake Status_3
Complete3
Data Toggle_4
Handshake Status_4
Complete4
Data Toggle_5
Handshake Status_5
Complete5
182c
Interrupt In Data_5
Interrupt In Data_4
Interrupt In Data_3
Interrupt In Data_2
Interrupt In Data_1
182d
Interrupt Out Data_5
Interrupt Out Data_4
Interrupt Out Data_3
Interrupt Out Data_2
Interrupt Out Data_1
182e
Classic Transaction Buffer

Figure 10a

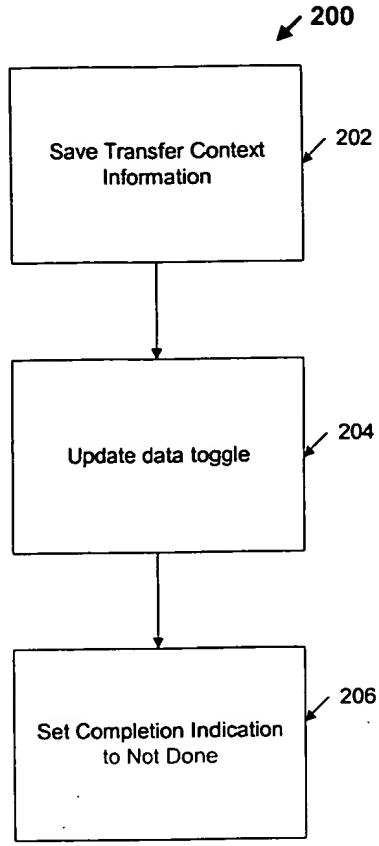


Figure 10b

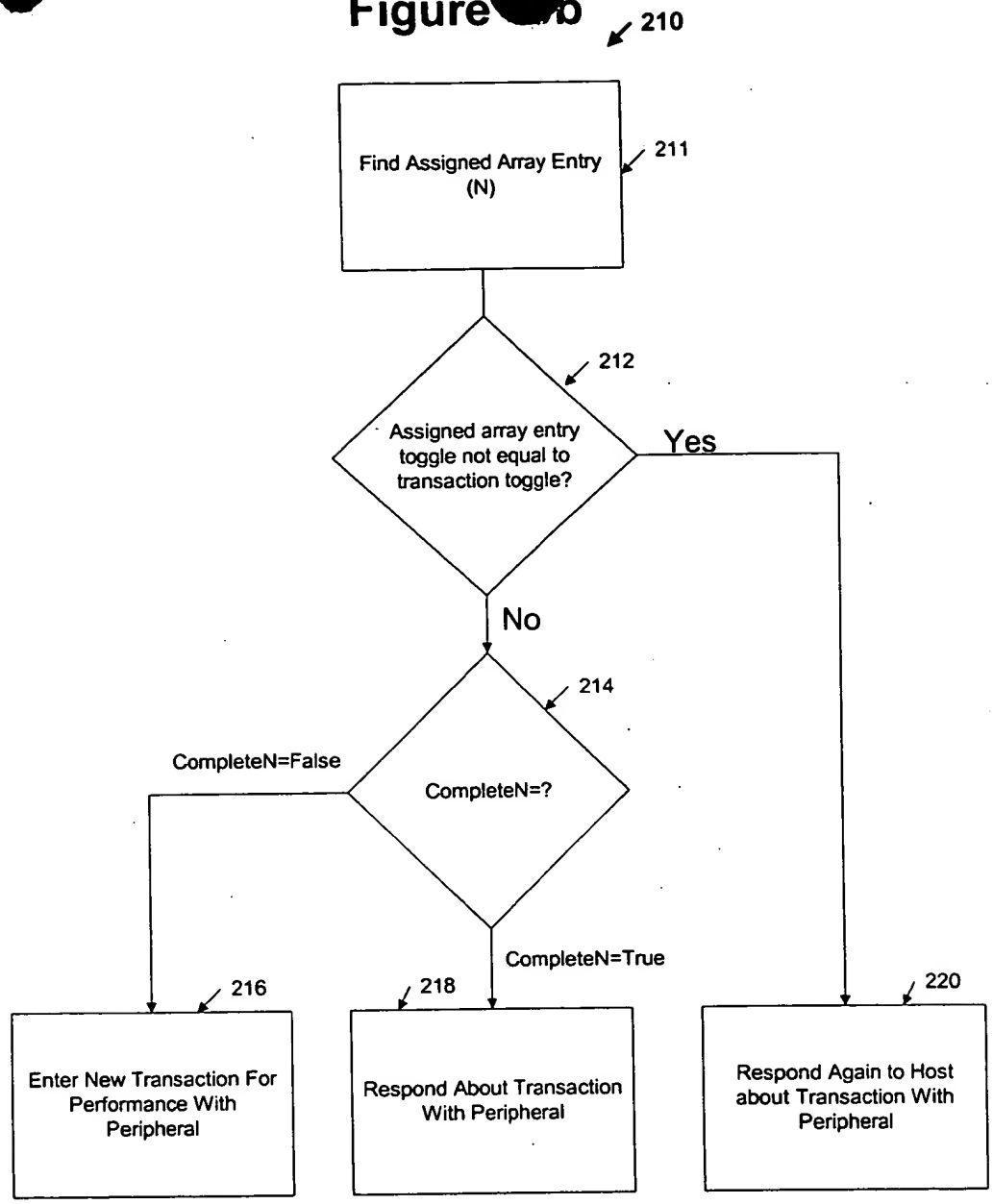


Figure 10c

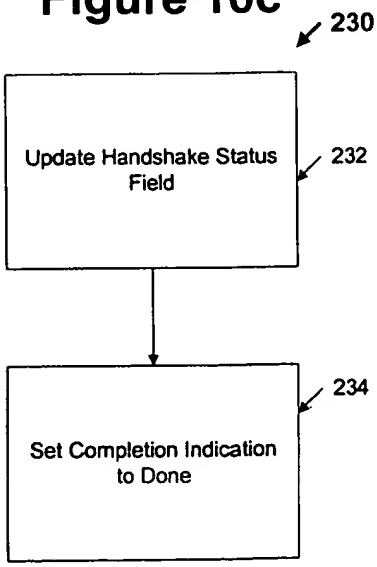
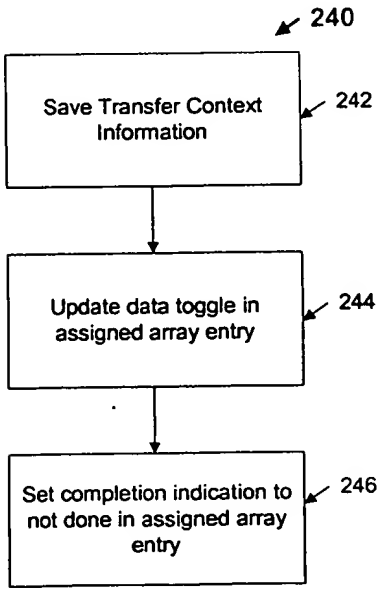



Figure 10d